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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: A7675

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Albert E. CASAVANT, et al.

MAY 11 2004

Appln. No.: 09/693,976

Group Art Unit: 2121

Technology Center 2100

Confirmation No.: 8676

Examiner: Aaron C. PEREZ DAPLE

Filed: October 23, 2000

For: PROPERTY-SPECIFIC TESTBENCH GENERATION FRAMEWORK FOR DESIGN
VALIDATION BY GUIDED SIMULATION

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith except for:

- 1) K.L. Mc Millan, Symbolic Model Checking, Kluwer Academic Publishers, 1993.
- 2) R.P. Kurshan, Computer Aided Verification of Coordinating Processes: The Automated Approach, Princeton University., Press 1995.

Applicant thanks the Examiner for obtaining copies of these references from the U.S. Patent and Trademark Office library. Therefore, a copy of these two references is not submitted.

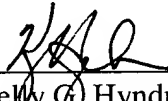
INFORMATION DISCLOSURE STATEMENT
U.S. Appln. No.: 09/693,976

The present Information Disclosure Statement is being filed (without a Statement Under 37 C.F.R. § 1.97(e)) after the later of three months from the application's filing date and the mailing date of the first Office Action on the merits, but before a Final Office Action, Notice of Allowance, or an action that otherwise closes prosecution in the application (whichever is earlier), and therefore a check for the fee of \$180.00 under 37 C.F.R. § 1.17(p) is attached.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,



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WASHINGTON OFFICE

23373

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Date: May 6, 2004

Substitute for Form 1449 A & B/PTO

Complete if Known

Application Number	09/693,976
Confirmation Number	8676
Filing Date	October 23, 2000
First Named Inventor	Albert E. CASAVANT
Art Unit	2121
Examiner Name	Aaron C. PEREZ DAPLE
Attorney Docket Number	A7675

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			

Technology Center 2100

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		F. Balarin, et al., "An iterative approach to language containment," In Proceedings of the International Conference on Computer-Aided Verification, volume 697 of Lecture Notes in Computer Science, pages 29--40, 1993.	
		R. K. Brayton et al., "VIS: A system for verification and synthesis", In R. Alur and T. Henzinger, editors, Proceedings of the International Conference on Computer-Aided Verification, volume 1102, pages 428--432. Springer-Verlag, June 1996.	
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		J. R. Burch, E. M. Clarke, D. E. Long, K. L. McMillan, and D. L. Dill, "Symbolic model checking for sequential circuit verification", IEEE Transactions on Computer-Aided Design, 13(4):401--424, Apr. 1994.	
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		A. Kuehlmann, et al., "Probabilistic state space search", In Proceedings of the International Conference on Computer-Aided Design, 1999.	

Examiner Signature

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Submitter for Form 1449 A & B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				<i>Complete if Known</i>	
				Application Number	09/693,976
				Confirmation Number	8676
				Filing Date	October 23, 2000
				First Named Inventor	Albert E. CASAVANT
				Art Unit	2123
				Examiner Name	Aaron C. PEREZ DAPLE
				Attorney Docket Number	A7675
Sheet	2	of	2		

U.S. PATENT DOCUMENTS					
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NON PATENT LITERATURE DOCUMENTS			
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Examiner Signature	Date Considered	RECEIVED
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